



P.C. Davis 23

AF
2811

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Paul Cooper Davis
Case: 23
Serial No.: 09/911,035
Filing Date: July 23, 2001
Group: 2811
Examiner: Quang D. Vu

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: Lucia M. Hankin Date: June 23, 2003

Title: Mixed Signal Integrated Circuit
with Improved Isolation

AMENDMENT TRANSMITTAL

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Sir:

Submitted herewith is the following document relating to the above-identified patent application:

- (1) Amendment After Final Rejection Under 37 C.F.R. §1.116.

There is no additional fee due in conjunction with the Amendment. In the event of any non-payment or improper payment of a required fee, the Commissioner is hereby authorized to charge or to credit **Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762** as required to correct the error.

Respectfully submitted,

Date: June 23, 2003

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Amdt
P.C. Davis
7/10/03

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Signature: Laura M. Hanin Date: June 23, 2003

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AMENDMENT AFTER FINAL REJECTION UNDER 37 C.F.R. §1.116

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Commissioner for Patents
P.O. Box 1450
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Sir:

In response to the final Office Action dated December 16, 2002, Applicant submits herewith the following Amendment for entry in the above-identified application. Applicant had previously filed a Notice of Appeal on April 16, 2003, which was received in the U.S. Patent and Trademark Office on April 22, 2003.

IN THE CLAIMS

Please cancel claims 1 and 26-29 without prejudice.

Please amend claims 2, 3, 6-8 and 15-17 by rewriting same as follows:

2. (Amended) The integrated circuit of claim 30, further comprising:

81 a plurality of conductive plugs formed in the substrate, the plugs providing a substantially low resistance path for electrically connecting the conductive layer to the isolation buried layer.